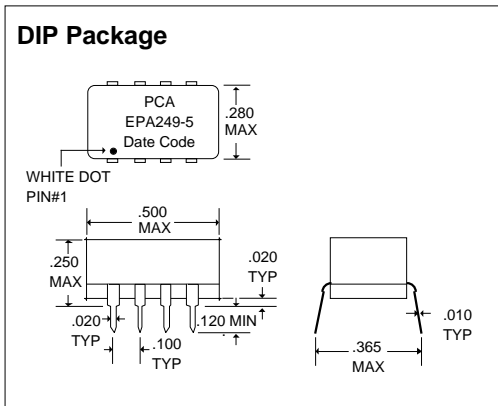


# 8 Pin Triple TTL Compatible Active Delay Lines

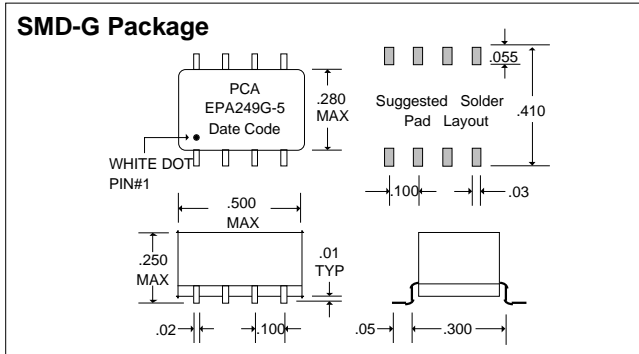
Delay Time ±5% or ±2nS†	DIP Part Number	SMD-G Part Number	SMD-J Part Number	Delay Time ±5% or ±2nS†	DIP Part Number	SMD-G Part Number	SMD-J Part Number
5	EPA249-5	EPA249G-5	EPA249J-5	23	EPA249-23	EPA249G-23	EPA249J-23
6	EPA249-6	EPA249G-6	EPA249J-6	24	EPA249-24	EPA249G-24	EPA249J-24
7	EPA249-7	EPA249G-7	EPA249J-7	25	EPA249-25	EPA249G-25	EPA249J-25
8	EPA249-8	EPA249G-8	EPA249J-8	30	EPA249-30	EPA249G-30	EPA249J-30
9	EPA249-9	EPA249G-9	EPA249J-9	35	EPA249-35	EPA249G-35	EPA249J-35
10	EPA249-10	EPA249G-10	EPA249J-10	40	EPA249-40	EPA249G-40	EPA249J-40
11	EPA249-11	EPA249G-11	EPA249J-11	45	EPA249-45	EPA249G-45	EPA249J-45
12	EPA249-12	EPA249G-12	EPA249J-12	50	EPA249-50	EPA249G-50	EPA249J-50
13	EPA249-13	EPA249G-13	EPA249J-13	55	EPA249-55	EPA249G-55	EPA249J-55
14	EPA249-14	EPA249G-14	EPA249J-14	60	EPA249-60	EPA249G-60	EPA249J-60
15	EPA249-15	EPA249G-15	EPA249J-15	65	EPA249-65	EPA249G-65	EPA249J-65
16	EPA249-16	EPA249G-16	EPA249J-16	70	EPA249-70	EPA249G-70	EPA249J-70
17	EPA249-17	EPA249G-17	EPA249J-17	75	EPA249-75	EPA249G-75	EPA249J-75
18	EPA249-18	EPA249G-18	EPA249J-18	80	EPA249-80	EPA249G-80	EPA249J-80
19	EPA249-19	EPA249G-19	EPA249J-19	85	EPA249-85	EPA249G-85	EPA249J-85
20	EPA249-20	EPA249G-20	EPA249J-20	90	EPA249-90	EPA249G-90	EPA249J-90
21	EPA249-21	EPA249G-21	EPA249J-21	95	EPA249-95	EPA249G-95	EPA249J-95
22	EPA249-22	EPA249G-22	EPA249J-22	100	EPA249-100	EPA249G-100	EPA249J-100

† Whichever is greater. Delay Times referenced from input to leading edges at 25°C, 5.0V, with no load.



### DC Electrical Characteristics

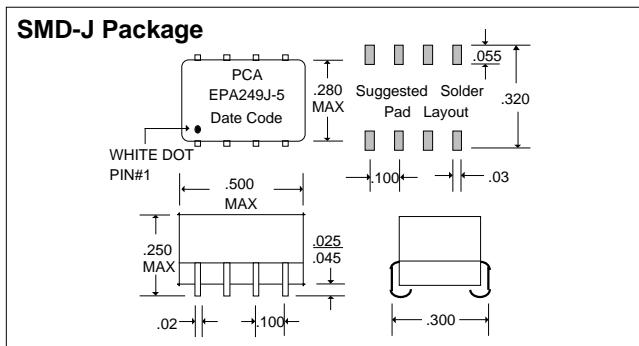
Parameter	Test Conditions	Min	Max	Unit
V <sub>OH</sub>	High-Level Output Voltage	V <sub>CC</sub> = min. V <sub>IL</sub> = max. I <sub>OH</sub> = max	2.7	V
V <sub>OL</sub>	Low-Level Output Voltage	V <sub>CC</sub> = min. V <sub>IH</sub> = min. I <sub>OL</sub> = max	0.5	V
V <sub>IK</sub>	Input Clamp Voltage	V <sub>CC</sub> = min. I <sub>I</sub> = I <sub>IK</sub>	-1.2V	V
I <sub>IH</sub>	High-Level Input Current	V <sub>CC</sub> = max. V <sub>IN</sub> = 2.7V	50	µA
		V <sub>CC</sub> = max. V <sub>IN</sub> = 5.25V	1.0	mA
I <sub>IL</sub>	Low-Level Input Current	V <sub>CC</sub> = max. V <sub>IN</sub> = 0.5V	-2	mA
I <sub>OS</sub>	Short Circuit Output Current	V <sub>CC</sub> = max. V <sub>OUT</sub> = 0. (One output at a time)	-40	mA
I <sub>CCH</sub>	High-Level Supply Current	V <sub>CC</sub> = max. V <sub>IN</sub> = OPEN	115	mA
I <sub>CCL</sub>	Low-Level Supply Current	V <sub>CC</sub> = max. V <sub>IN</sub> = 0	115	mA
T <sub>RO</sub>	Output Rise Time		4	nS
N <sub>H</sub>	Fanout High-Level Output	V <sub>CC</sub> = max. V <sub>OH</sub> = 2.7V	20	TTL LOAD
N <sub>L</sub>	Fanout Low-Level Output	V <sub>CC</sub> = max. V <sub>OL</sub> = 0.5V	10	TTL LOAD



### Recommended Operating Conditions

\*These two values are inter-dependent.

	Min	Max	Unit
V <sub>CC</sub>	4.75	5.25	V
V <sub>IH</sub>	2.0		V
V <sub>IL</sub>		0.8	V
I <sub>IK</sub>		-18	mA
I <sub>OH</sub>		-1.0	mA
I <sub>OL</sub>		20	mA
PW*	40		%
d*		40	%
T <sub>A</sub>	0	+70	°C



### Input Pulse Test Conditions @ 25° C

	Unit
E <sub>IN</sub>	3.2 Volts
PW	110 %
T <sub>RI</sub>	2.0 nS
PRR	1.0 MHz
V <sub>CC</sub>	5.0 Volts

