SOT23 Surface Mount Voltage Divider



New DIV23 Series

- Replaces IRC SOT23 Series for new designs
- Precision ratio tolerances to ±0.05%
- Superior alternative to matched sets
- Ultra-stable TaNSil® resistors on silicon substrate
- · RoHS Compliant and Sn/Pb terminations available



Electrical Data

Element Resistance Range	10 to 200KΩ		
Absolute Tolerance	To ±0.1%		
Ratio Tolerance to R1	To ±0.05%		
Absolute TCR	To ±25ppm/°C		
Tracking TCR	To ±2ppm/°C		
Element Power Rating @ 70°C	100mW		
Package Power Rating @ 70°C	1.0W		
Rated Operating Voltage (not to exceed √P x R)	100 Volts		
Operating Temperature	-55°C to +125°C		
Noise	<-30dB		

Environmental Data

Test Per MIL-PRF-83401	Typical Delta R	Max Delta R
Thermal Shock	±0.02%	±0.1%
Power Conditioning	±0.03%	±0.1%
High Temperature Exposure	±0.03%	±0.05%
Short-time Overload	±0.02%	±0.05%
Low Temperature Storage	±0.03%	±0.05%
Life	±0.05%	±2.0%

Manufacturing Capability

Individual Resistance	Available Absolute Tolerances	Available Ratio Tolerances	Best Absolute TCR	Tracking TCR
10Ω - 25Ω	FGJK	DFG	±100ppm/°C	±25ppm/°C
25.1Ω - 50Ω	DFGJK	CDFG	±50ppm/°C	±10ppm/°C
51Ω - 500Ω	CDFGJK	BCDFG	±25ppm/°C	±2ppm/°C
501Ω - 100ΚΩ	BCDFGJK	ABCDFG	±25ppm/°C	±2ppm/°C
101ΚΩ - 200ΚΩ	BCDFGJK	BCDFG	±25ppm/°C	±2ppm/°C

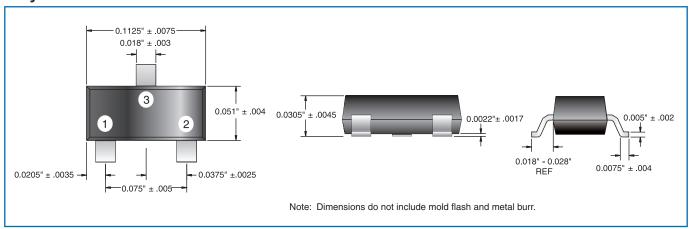
General Note
IRC reserves the right to make changes in product specification without notice or liability. All information is subject to IRC's own data and is considered accurate at time of going to print.



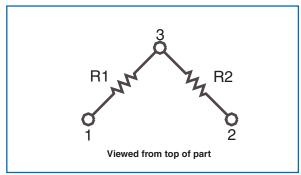
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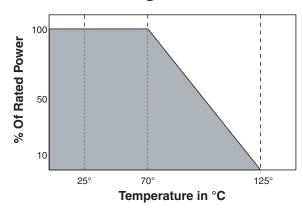
Physical Data



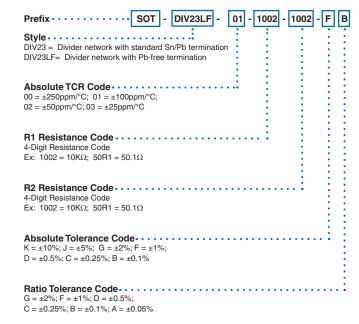
Schematic Data



Power Derating Data



Ordering Procedure



Packaging

Standard packaging is tape & reel.

SOT23 Series Legacy Design Note
The SOT-DIV23 place R1 between pins 1 and 3 and R2 between pins 2 and 3. The SOT-SOT23 place R1 between pins 2 and 3 and R2 between pins 1 and 3.

For additional information or to discuss your specific requirements. please contact our Applications Team using the contact details below.