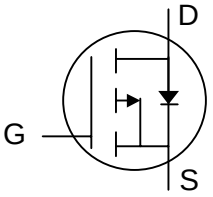
P-channel Enhancement-mode Power MOSFET

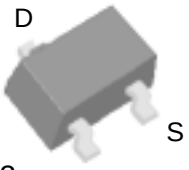
- Low gate-charge
- Simple drive requirement
- Fast switching
- **Pb-free; RoHS compliant.**



BV_{DSS}	-30V
$R_{DS(ON)}$	80mΩ
I_D	-3.2A

DESCRIPTION

The SSM2305AGN is in a SOT-23-3 package, which is widely used for lower power commercial and industrial surface mount applications. This device is suitable for low-voltage applications such as DC/DC converters and and general switching applications.



SOT-23-3

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	± 12	V
I_D @ T_A=25°C	Continuous Drain Current[3]	-3.2	A
I_D @ T_A=70°C	Continuous Drain Current[3]	-2.6	A
I_{DM}	Pulsed Drain Current[1,2]	-10	A
P_D @ T_A=25°C	Total Power Dissipation	1.38	W
	Linear Derating Factor	0.01	W/°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

THERMAL DATA

Symbol	Parameter	Value	Unit
$R_{\Theta JA}$	Maximum Thermal Resistance, Junction-ambient[3]	90	°C/W

ELECTRICAL CHARACTERISTICS (at Tj = 25°C unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=-250uA	-30	-	-	V
$\Delta BV_{DSS}/\Delta Tj$	Breakdown Voltage Temperature Coefficient	Reference to 25°C, I_D=-1mA	-	-0.1	-	V/°C
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=-10V, I_D=-3.2A	-	-	60	mΩ
		V_{GS}=-4.5V, I_D=-3.0A	-	-	80	mΩ
		V_{GS}=-2.5V, I_D=-2.0A	-	-	150	mΩ
		V_{GS}=-1.8V, I_D=-1.0A	-	-	250	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=-250uA	-0.5	-	-1.2	V
g_{fs}	Forward Transconductance	V_{DS}=-5V, I_D=-3.0A	-	9	-	S
I_{DSS}	Drain-Source Leakage Current (T_j=25°C)	V_{DS}=-30V, V_{GS}=0V	-	-	-1	uA
	Drain-Source Leakage Current (T_j=70°C)	V_{DS}=-24V, V_{GS}=0V	-	-	-25	uA
I_{GSS}	Gate-Source Leakage	V_{GS}= ± 12V	-	-	±100	nA
Q_g	Total Gate Charge[2]	I_D=-3.2A	-	10	18	nC
Q_{gs}	Gate-Source Charge	V_{DS}=-24V	-	1.8	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	V_{GS}=-4.5V	-	3.6	-	nC
$t_{d(on)}$	Turn-on Delay Time[2]	V_{DS}=-15V	-	7	-	ns
t_r	Rise Time	I_D=-3.2A	-	15	-	ns
$t_{d(off)}$	Turn-off Delay Time	R_G=3.3Ω , V_{GS}=-10V	-	21	-	ns
t_f	Fall Time	R_D=4.6Ω	-	15	-	ns
C_{iss}	Input Capacitance	V_{GS}=0V	-	735	1325	pF
C_{oss}	Output Capacitance	V_{DS}=-25V	-	100	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	80	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage[2]	I_S=-1.2A, V_{GS}=0V	-	-	-1.2	V
trr	Reverse Recovery Time	I_S=-3.2A, V_{GS}=0V,	-	24	-	ns
Qrr	Reverse Recovery Charge	dI/dt=100A/μs	-	19	-	nC

Notes:

1.Pulse width limited by maximum junction temperature.

2.Pulse width ≤300us, duty cycle ≤2%.

3.Surface-mounted on 1 in^2 copper pad on FR4 board ; 270°C/W when mounted on minimum copper pad.

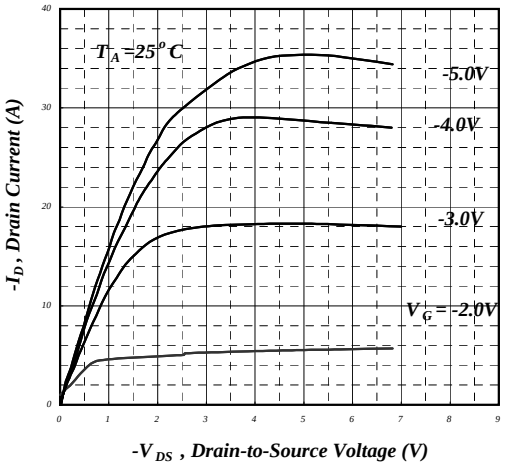


Fig 1. Typical Output Characteristics

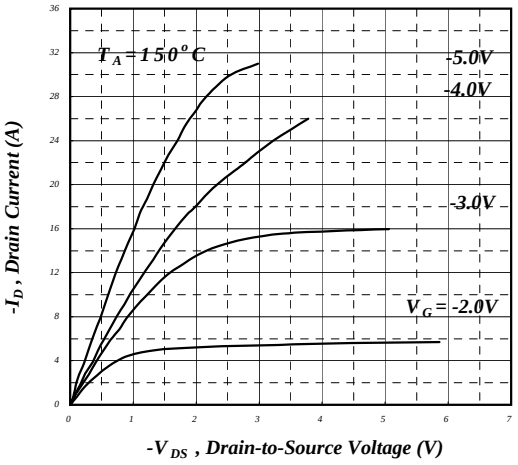


Fig 2. Typical Output Characteristics

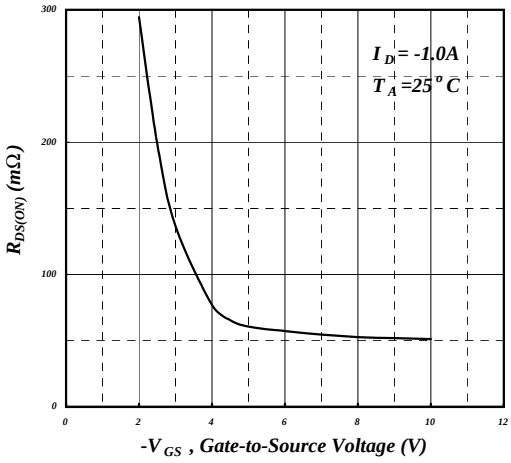


Fig 3. On-Resistance vs. Gate Voltage

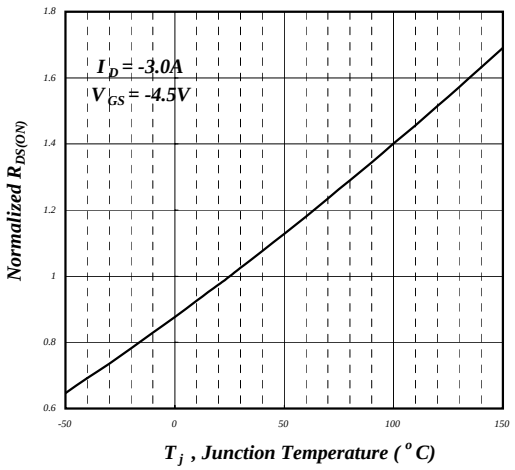


Fig 4. Normalized On-Resistance vs. Junction Temperature

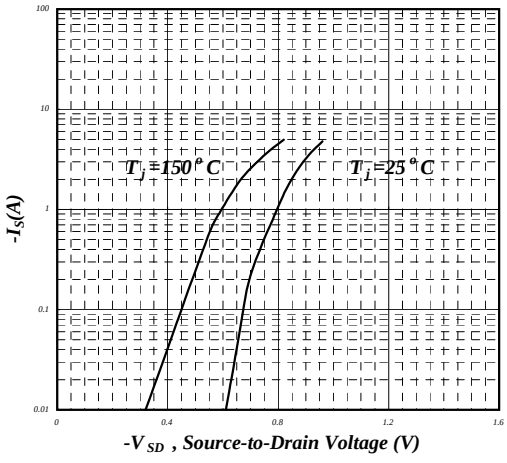


Fig 5. Forward Characteristic of Reverse Diode

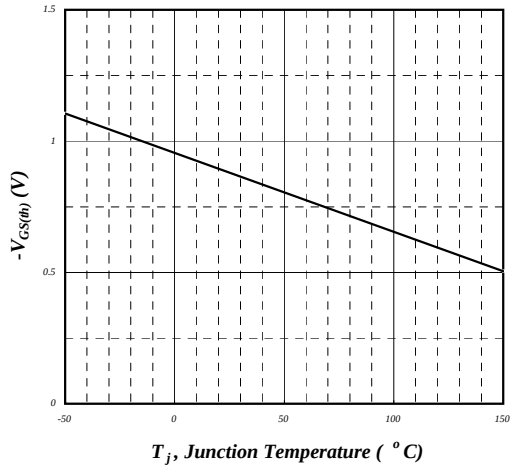


Fig 6. Gate Threshold Voltage vs. Junction Temperature

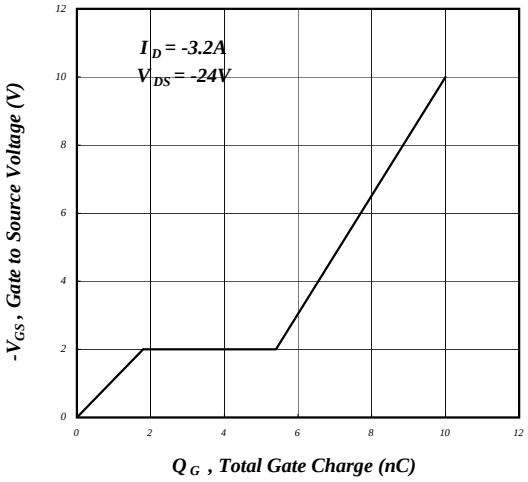


Fig 7. Gate Charge Characteristics

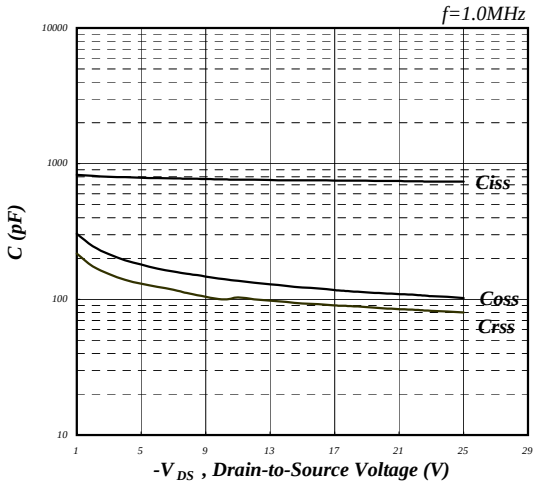


Fig 8. Typical Capacitance Characteristics

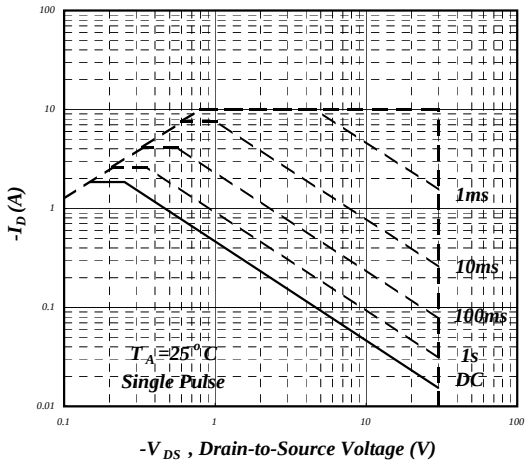


Fig 9. Maximum Safe Operating Area

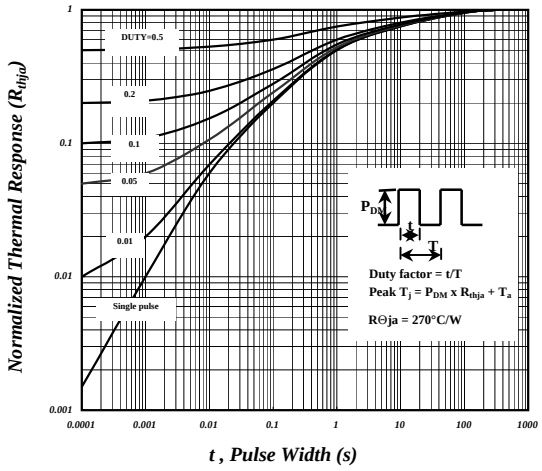


Fig 10. Effective Transient Thermal Impedance

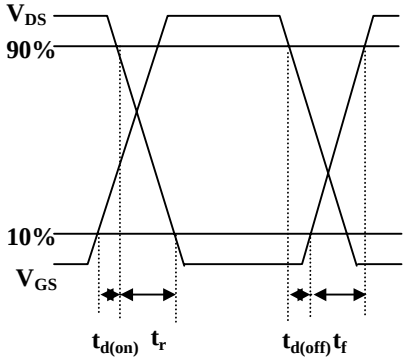


Fig 11. Switching Time Circuit

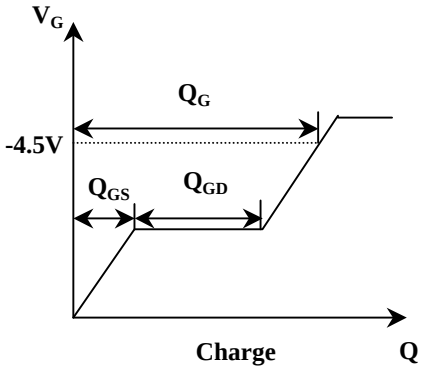


Fig 12. Gate Charge Circuit