

WTPA12A60SW

Sensitive Gate Bi-Directional Triode Thyristor

Features

- Repetitive Peak off -State Voltage:600V
- R.M.S On-State Current(IT(RMS)=12A)
- Low On-State Voltage (1.55V(Max.)@I_{TM}
- High Commutation dv/dt
- Halogen free(WTPA12A60SW-HF)



General Description

General purpose switching and phase control applications .These devices are intented to be interfaced directly to miro-Controllers,logic integrated circuits and other low power gatetrigger circuits such as fan speed and temperature modulation control,lighting control and static switching relay. By using an internal ceramic pad, the WTPA series provides voltage insulated tab (rated at 2500V RMS) complying with UL standards (file ref.:E347423)



Absolute Maximum Ratings (TJ=25°C unless otherwise specified)

symbol	Parameter			Units
V _{DRM} / _{VPRM}	Peak Repetitive Forward Blocking Voltage(gate open) (Not	600	V	
I _{T(RMS)}	Forward Current RMS(All Conduction Angles, T_J=58 $^\circ\!\!\!\!^\circ C)$	12	А	
Ітѕм	Peak Forward Surge Current, (full Cycle, Sine Wave,50/60H	120/126	А	
l ² t	Circuit Fusing Considerations (tp=10ms)	100	A ² s	
Рдм	Peak Gate Power —Forward,(TJ=58°C,Pulse With≤1.0us)		5	W
P _{G(AV)}	Average Gate Power — Forward, (Over any 20ms period)		1.0	W
dl/dt	Critical rate of rise of on-state current	T -125°0	50	A/µs
	I _{TM} =20A;I _G =200mA;dI _G /dt=200mA/µs	IJ=125 C		
I _{FGM}	Peak Gate Current—Forward,TJ=125°C(20µs,120PPS)		4	А
V _{RGM}	Peak Gate Voltage—Reverse,TJ=125 [°] C(20µs,120PPS)		10	V
TJ	Junction Temperature		-40~125	Ĵ
T _{stg}	Storage Temperature		-40~150	°C

Note1.Although not recommended off -state voltages up to 800v ,may be applied with out damage, but the TRIAC may swiTJh to the on-state .the rate of rise of current should not exceed 15A/us.

Thermal Characteristics

Symbol	Parameter	Value	Units
Rejc	Thermal Resistance Junction to case	2.3	°C/W
Reja	Thermal resistance Junction to Ambient	60	°C/W

Rev.A May.2011



Symbol	Characteristics		Min.	Тур.	Max.	Unit
Idrm/Irrm	Peak Forward or Reverse Blocking Current	T J=25 ℃	-	-	5	μA
	(V _{DRM} =V _{RRM})	TJ=125℃	-	-	1	mA
V _{TM}	Forward "On" Voltage (Note2) (I _{TM} =17A tp=380µs)		-	-	1.55	V
	Gate Trigger Current (Continuous dc)	T2+G+	-	-	10	
I _{GT}	$(V_D=12 \text{ Vdc}, R_L=30\Omega)$	T2+G-	-	-	10	mA
		T2-G-	-	-	10	
	Gate Trigger Voltage (Continuous dc)	T2+G+	-	-	1.2	
V _{GT}	$(V_D=12 \text{ Vdc}, R_L=30\Omega)$	T2+G-	-	-	1.2	V
		T2-G-	-	-	1.2	
V _{GD}	Gate threshold voltage (V_D=V_DRM,RL=3.3K Ω ,TJ=125 °C)		0.2	-	-	V
dV/dt	Critical rate of rise of commutation Voltage(V_D =0.67 V_{DRM})		40	-	-	V/µs
Iн	Holding Current (I _T =100mA)		-	-	15	mA
IL.	Latching current	T2+G+	-	-	25	
	(V _D =12Vdc,I _{GT} =1.2 I _{GT})	T2+G-	-	-	30	mA
		T2-G-	-	-	25	
Rd	Dynamic resistance		-	-	35	mΩ

Electrical Characteristics(Tc=25°C unless otherwise noted)

Note2.Forward current applied for 1 ms maximum duration ,duty cycle

Note3.For both polarities of A2 to A1



ITSM (A) 130 120 110 100 90 Ti initial=25°C 80 70 60 50 Repetitive Tc=90°C 40 30 20 Number of cycles 10 0 10 100 1000 1

Fig .1 Maximum permissible non-repetitive peak on-state current I_{TSM}, versus number of cycles, for Sinusoidal currents, f=50Hz.



*Fig . 3 Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms, and corresponding value off*²*t*



Fig.5 Typical and maximum on-state characteristic.



P (W)



Fig. 2Maximum on-state dissipation, P_{tot} , versus rms on-state current, $I_{T(RMS)}$, where α =conduction angle.



Fig.4 Maximum permissible rms current I_{T(RMS)}, Versus lead temperature T_{lead}.





W





Fig.7 Relative variation of critical rate of decrease of main current versus junction temperature.



Fig.8 Normalised gate trigger voltage $V_{GT}(T_j)/V_{GT}(25^{\circ}C)$ Versus junction temperature T_j



Fig.9 Transient thermal impedance Z_{th j-mb}, versus pulse width t_p



Marking layout

WTPA12A60SW



- : Winsemi Semiconductor Logo
- S : IGT
- Δ : W:The third quadrant

Null : The fourth quadrant

- WW : Weekly code(01-52)
- YY: Last two digit of calendar year

(11:2011;12:2012)

- □ : HF Halogen free
 - Null Halogen



W

TO-220 Package Dimension

